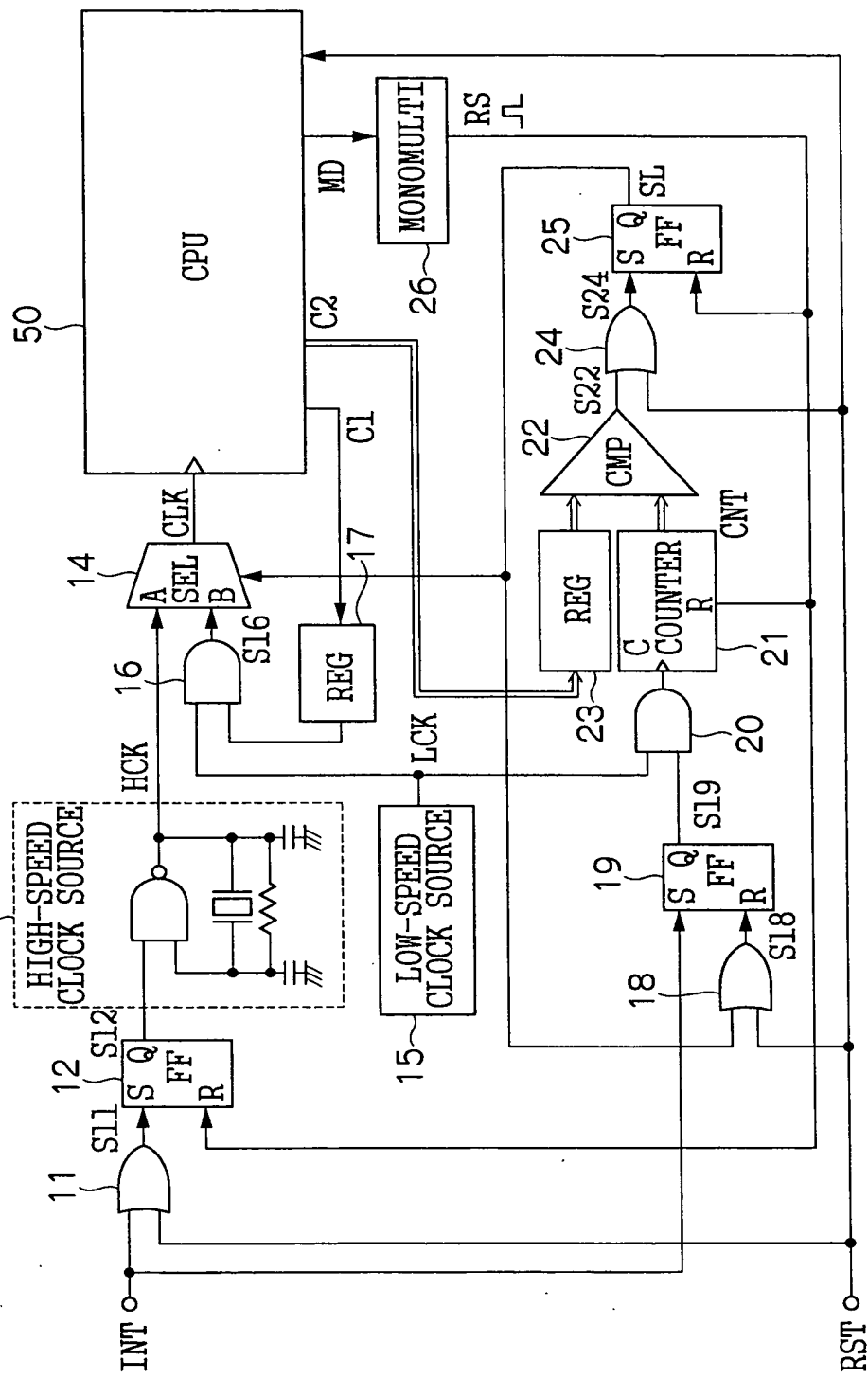
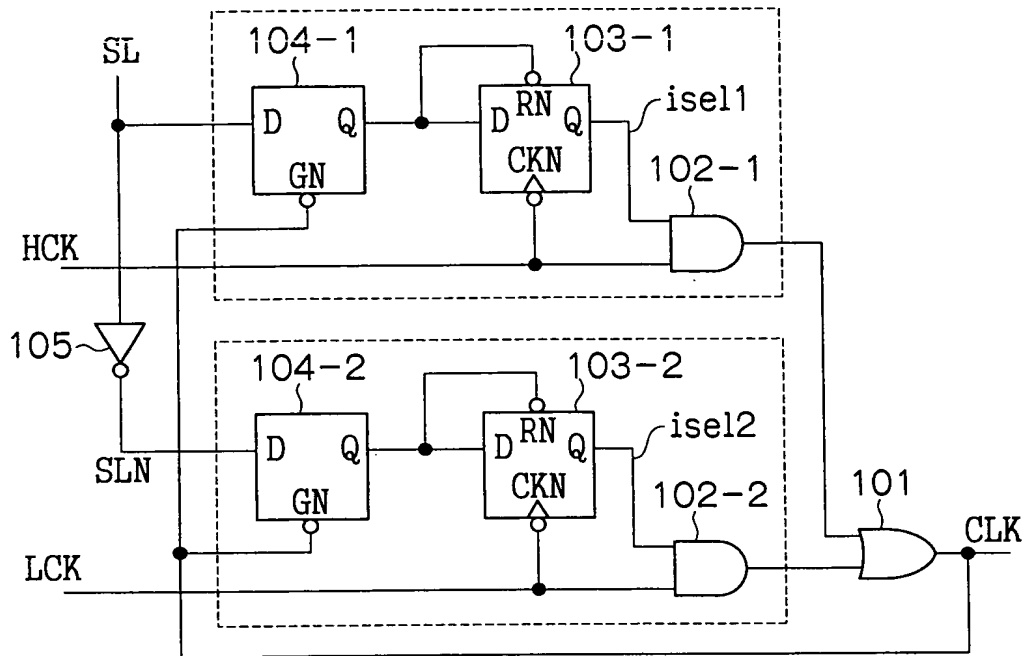


FIG. 1



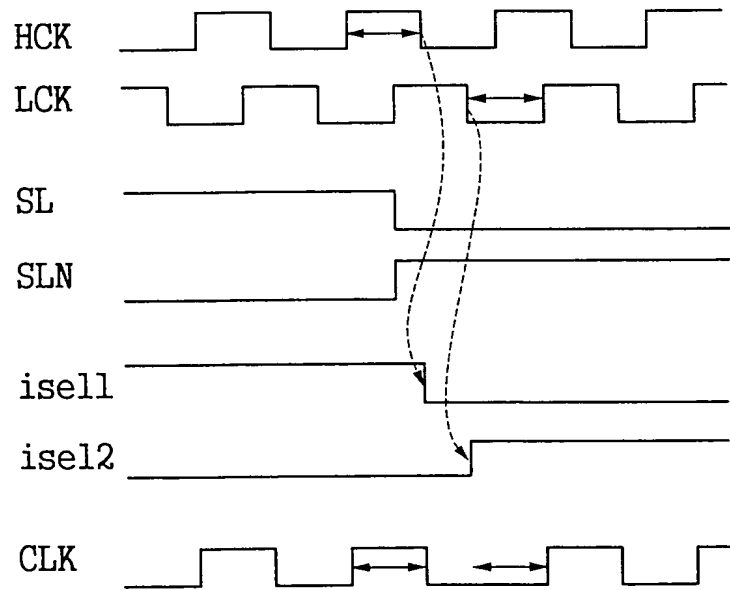
CLOCK CONTROL CIRCUIT OF THE FIRST EMBODIMENT OF THE PRESENT INVENTION

FIG.2A



CIRCUIT STRUCTURE

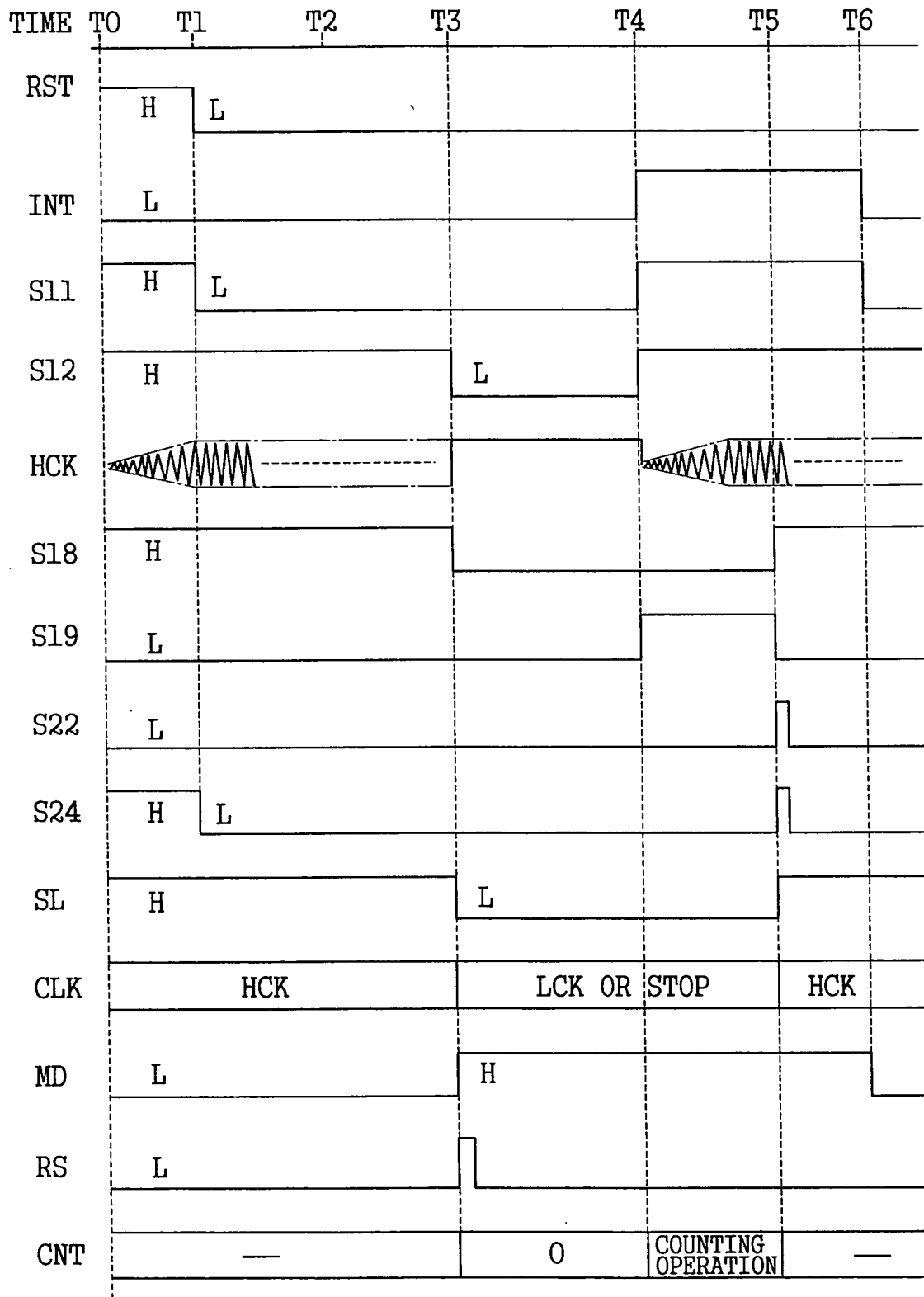
FIG.2B



OPERATION

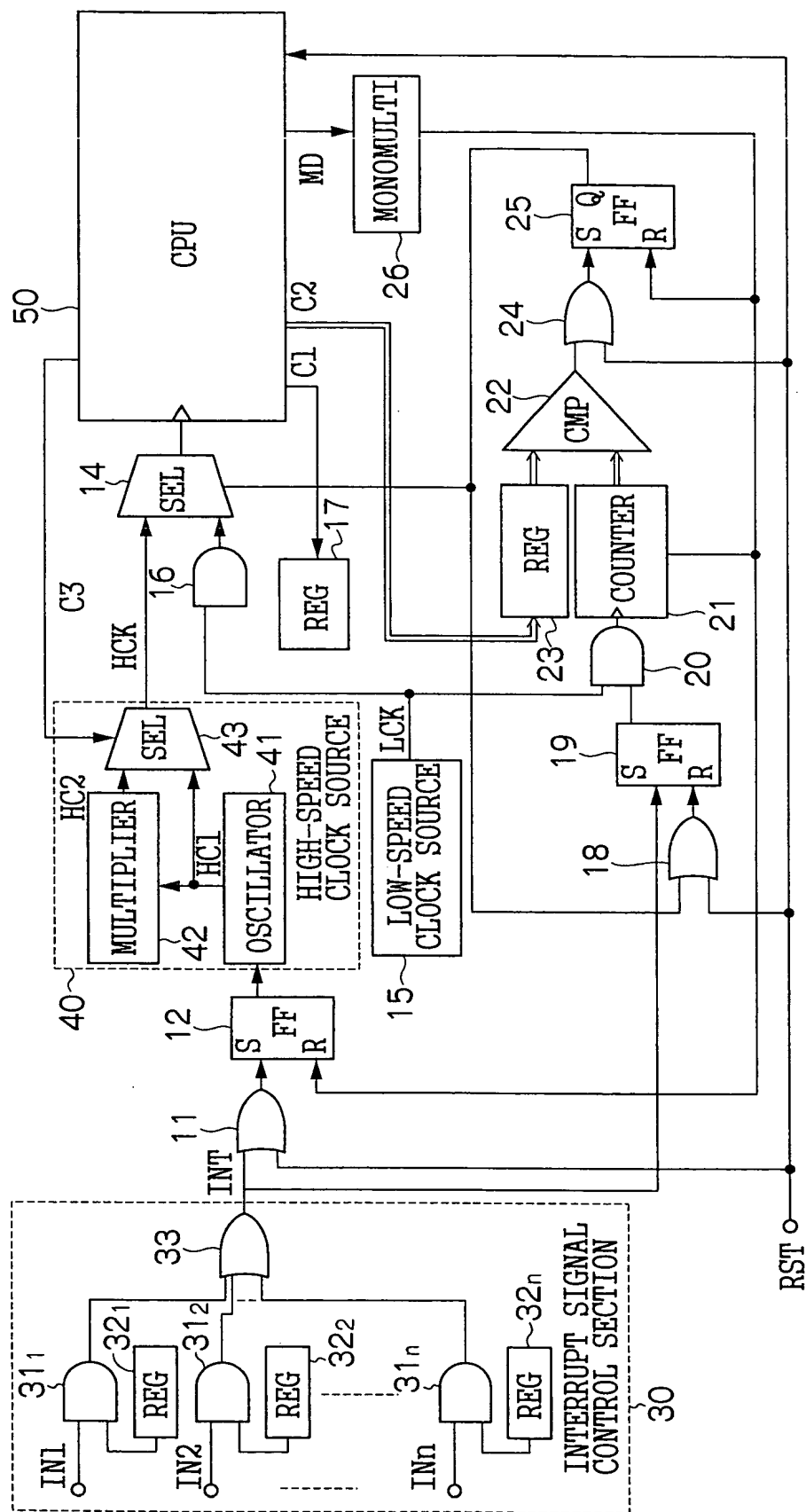
EXAMPLE OF SELECTOR 14 IN FIG. 1

FIG.3



OPERATION OF FIG. 1

FIG.4



CLOCK CONTROL CIRCUIT OF THE SECOND EMBODIMENT OF THE PRESENT INVENTION